Wiring logic for side-access μLB

μ AC FV e_{B1} e_{B2} e_{F1} e_{F2} e_{Ui} e_{Ux} e_{Di} e_{Dx}

CFD (×11)

AC

μ_{inc} Scaler

$*$

V_s

e_B e_F e_U e_D

TMin

in $\overline{P}$
$\overline{B}$
Pileup Gate

μ_g

TrigVeto

uG   Scaler

Data Gate
H   DataGate

G

250 ns   250 ns   250 ns   250 ns

OR
1
2
3
4

TDC Gate
H+400 ns   TDCGate

TM Out

GGL

e_{Bg} e_{Fg} e_{Ug} e_{Dg}
Scaler   Scaler   Scaler   Scaler

TDC
Ref

TDC
gate

TDC
in 0

TDC
in 1

TDC
in 2

TDC
in 3
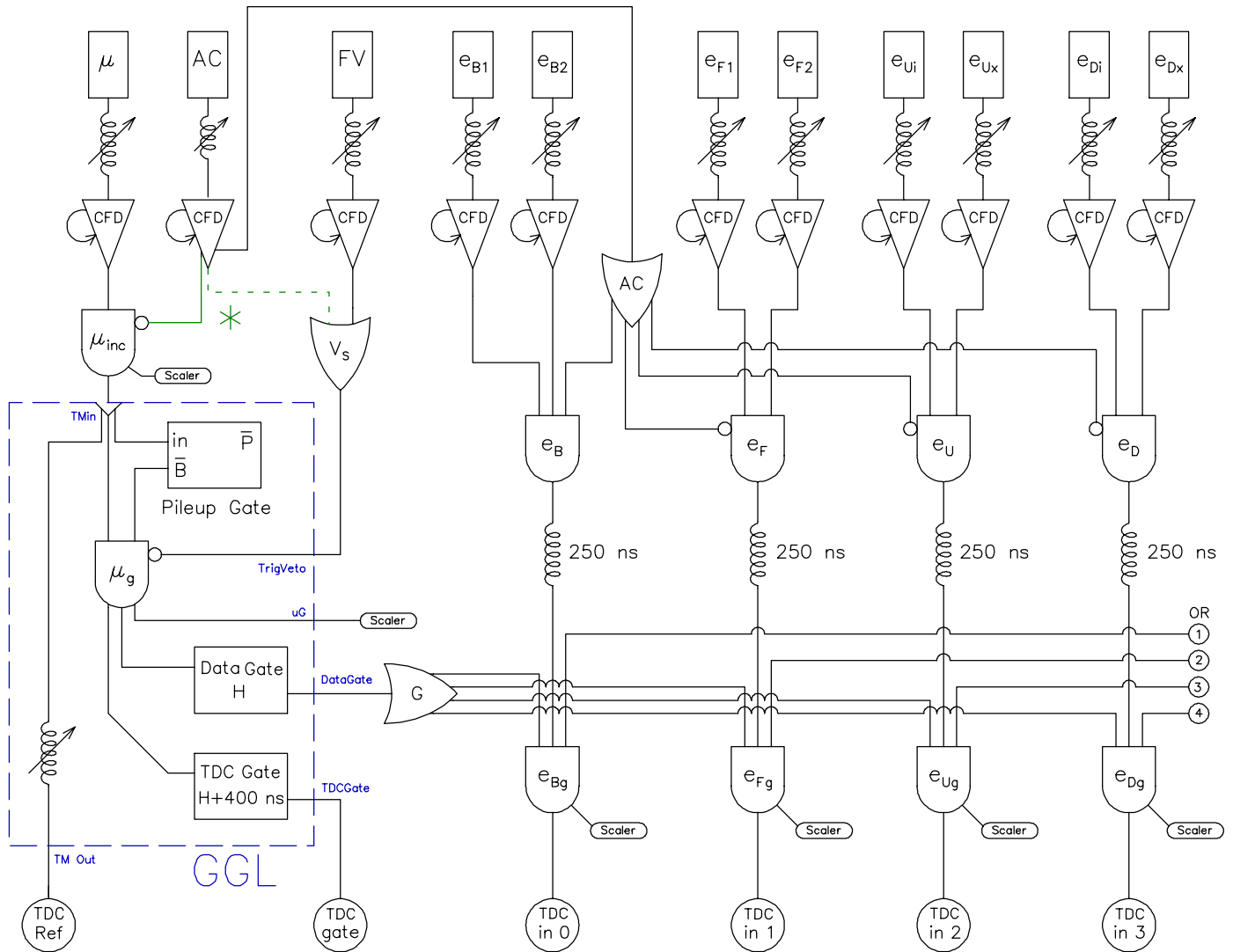
TIMING:
1) Set AC to a moderate delay
2) Adjust μ to be vetoed by AC (at GGL or at μ_{inc})
3) Adjust FV to veto μ_{inc} at GGL
4) Adjust each e telescope to match AC

$*$) AC may veto either μ–stop or μ–gated, depending on which
   problem is worse: If too many muons scatter off the edge of the
   hole in the AC to stop in the μ counter, then the AC should
   veto μ_g (fan in with FV). Usually, the bigger problem is that
   some decay positrons trigger the μ counter, causing a "step"
   at early times; then the AC should veto μ_{inc}.